REGISTRATION FORM

A One week Faculty Development Programme on

"Implementation of Digital Logic Design Using Verilog Coding and RTL Synthesis"

01st – 06th April 2021

1. Name :

2. Qualification :

3. Organization : Industry / Institute

4. Designation :

5. Department :

6. Name of the :

7. Category : Research Scholar / Faculty

8. Accommodation : Yes / No

9. Email ID :

10. Phone No :

Date: **Signature of Candidate**

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CHIEF PATRON
Sri. Y.Vinay Kumar
Management Secretary
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Director
Dr. V.Ravi Prasad Naidu
Principal
Co-PATRONS
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Vice- Principal
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CO-ORDINATOR
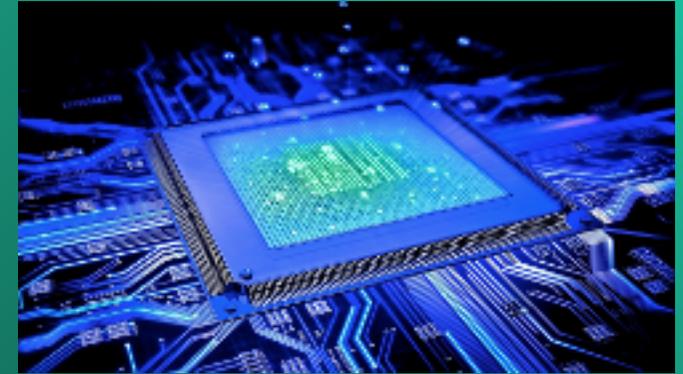Dr. P. Rajasekar, Professor

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16.	Mr. S.Brahmaiah	Asst. Professor
17.	Ms. T.Y. Lakshmi	Asst. Professor
18.	Mr.M V S Sudheer Babu	Asst. Professor

A one Week Faculty Development Programme

On

"Implementation of Digital Logic Design Using Verilog Coding and RTL Synthesis"

01st – 06th April 2021

Organized by

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

NARAYANA ENGINEERING COLLEGE

(AUTONOMOUS)

Dhoorjati Nagar, GUDUR,

SPSR Nellore Dt.,Andhra Pradesh – 524101

About the institute:

Narayana Engineering College is one of the premier engineering colleges in Andhra Pradesh sponsored by Narayana Group of Educational Institutions. The college was established in the year 2001 with a vision to impart quality technical education. Our college is permanently affiliated to JNTUA, Anantapuramu, and conferred UGC Autonomous Status and completed 20 years of successful existence in technical education. Accredited by NAAC with 'A'.

The College is located in India's Southern State, Andhra Pradesh. This college is located near to the National Highway (NH-16) in GUDUR. The nearest airport is Chennai, which 2 hours' drive. The college has been conducting B.Tech Courses in Electronics & Communication, Computer Science Engineering, Electrical & Electronics, Mechanical Engineering and Civil Engineering. The College also offers Post Graduate programs M.Tech in DECS, PE & CS, MCA and MBA. Our motto is to provide the students an excellence in their career and our goal is to shape their future in all aspects.

About the Department:

The aim of the department is to produce Electronics and Telecommunication Engineers, who can combine academic excellence with practical expertise. Our department of Electronics and Communication Engineering was introduced in the year 2001 with the approval of the All-India Council for Technical Education (AICTE). The department has progressed rapidly and is now regarded as one of the premiere departments, with excellent infrastructure and a highly qualified experienced and dedicated faculty.

At present, the department is offering B.Tech (ECE) and M.Tech (DECS) with total intake of 150 and 9 respectively.

The majority of our students have been well placed in many companies through campus interviews and many other students are pursuing their higher education at various premier institutions like IIT'S, NIT'S and also at abroad.

The department involves in teaching and research in diverse aspects of Communications, VLSI design, Embedded Systems, Signal Processing, Image Processing and Neural Networks. Apart from teaching, the faculty members are vigorously pursuing research and are regularly publishing papers in various National and International Journals of repute as well as in prestigious conferences.

About the FDP:

The main purpose of this FDP is to provide opportunity for Engineering faculty from Academic institutions, and for post graduate students to get exposure and interaction with the field experts, who are involved in the research and development activities in Signal and image processing. Technical persons from industry and reputed institutions will be invited to provide exposure to recent topics of Designing and simulation of Algorithms in Signal and image processing using MATLAB and SIMULINK tools.

FDP will also give impetus to participants to take up their research development activities in the field of Signal and Image processing, therefore solutions for many real-world problems will be attempted in an efficient way.

Eligibility:

- Faculty of Educational Institutions / Research Scholars

- .Persons working in R&D Organizations, with a minimum qualification of Bachelor's degree in Electronics & Communication / Electrical branches of Engineering / PG Students are eligible to participate.

Major source contents:

- Evolution of Electronics & VLSI Design.
- CMOS Digital IC Design
- Advances in VLSI and Nanoelectronics
- VLSI circuits and systems for AI and machine learning
- Mixed Signal VLSI Design
- VLSI for Signal Processing
- Bioinspired VLSI System
- ASIC and SoC Design

Resource Person:

The sessions will be conducted by invited experts from Utilities/ Industries/ Research Institutions and experienced KARE faculty members.

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